Comparison between **STM32L152RCT6** and **STM32L476VGT6**

	STM32L1	STM32L4
Core	Cortex-M**3** @ 32MHz with **64** pins	Cortex-M**4** @ 80MHz with **FPU and DSP** with **100** pins
MSI	64 kHz, 128 kHz, 256 kHz, 512 kHz, 1.02 MHz, **2.05 MHz (default value)**, 4.1 MHz	100 kHz, 200 kHz, 400 kHz, 800 kHz, 1 MHz, 2 MHz, **4 MHz (default value)**, 8 MHz, 16 MHz, 24 MHz, 32 MHz and 48 MHz
LSI	37 kHz	32 kHz RC
HSE	1 – 24 MHz	4 – 48 MHz
System clock	Up to 32 MHz Default to **MSI 2MHz** after reset	Up to 80 MHz Default to **MSI 4MHz** after reset
RCC	RCC_AHBENR	RCC_AHB**1**ENR (AHB1) RCC_AHB**2**ENR (AHB2) RCC_AHB**3**ENR (AHB3)
	RCC_AHB**LP**ENR (LP = Low Power)	RCC_AHB**1SM**ENR (AHB1) RCC_AHB**2SM**ENR (AHB2) RCC_AHB**3SM**ENR (AHB3) (SM = Sleep Mode)
	RCC_APB1ENR	RCC_APB1ENR**1** RCC_APB1ENR**2**
	RCC_APB1**LP**ENR (LP = Low Power)	RCC_APB1**SM**ENR**1** RCC_APB1**SM**ENR**2** (SM = Sleep Mode)
	RCC_APB2**LP**ENR (LP = Low Power)	RCC_APB2**SM**ENR (SM = Sleep Mode)
	The MSIRANGE in **RCC_ICSCR** selects the MSI frequency.	The MSIRANGE in **RCC_CR** or **RCC_CSR** selects the MSI frequency. The MSIRGSEL bit in RCC_CR determines which MSIRANGE is used. • If MSIRGSEL is 0 (default), the MSIRANGE in RCC_CSR is used to select the MSI clock range. • If MSIRGSEL is 1, the MSIRANGE in RCC_CR is used.